

## AMENDMENTS TO THE CLAIMS

1. (Currently Amended) A video signal processor comprising:
  - a memory that utilizes a first clock signal for writing a video data signal to the memory and a second clock signal for reading a video data signal from the memory;
  - a delay unit, including plural delay elements, for delaying the second clock signal;
  - a selector for selecting a clock that is most synchronized with a reference signal inputted from outside, from among clocks that have been delayed ~~by~~using the ~~respective~~ delay elements, ~~respectively of the delay unit~~, and ~~outputs~~outputting phase information of the selected clock;
  - an interpolation factor output unit for converting the phase information into an interpolation factor, and outputting the interpolation factor; and
  - an interpolator for interpolating the video data signal read from the memory in accordance with the second clock signal using the interpolation factor.
  
2. (Currently Amended) A video signal processor comprising:
  - a memory that utilizes a first clock signal for writing a video data signal to the memory and a second clock signal for reading a video data signal from the memory;
  - a delay unit, including plural delay elements ~~having that can vary~~ respective variable delay ~~values~~elements, for delaying the second clock signal by one period of the second clock signal;
  - a phase comparator for comparing phases between a clock that is obtained by delaying a focus clock in the second clock signal by one clock using the delay unit, and a clock that is one clock later in the second clock signal than the focus clock;
  - a controller for controlling ~~the~~ respective variable delay values of the delay elements of the delay unit on the basis of a phase difference detected by the phase comparator;
  - a selector for selecting a clock that is most synchronized with a reference signal inputted from outside, from among clocks that have been delayed by the ~~respective~~ delay elements, respectively of the delay unit, and outputting phase information of the selected clock;
  - an interpolation factor output unit for converting the phase information into an interpolation factor, and outputting the interpolation factor; and

an interpolator for interpolating the video data signal read from the memory in accordance with the second clock signal using the interpolation factor.

3. (Currently Amended) A video signal processor comprising:

a memory that utilizes a first clock signal for writing ~~or reading~~ a video data signal to the memory or reading a video data signal from the memory;

a delay unit, including plural delay elements, for delaying the first clock signal;

a selector for selecting a clock that is most synchronized with a reference signal inputted from outside, from among clocks that have been delayed ~~by using~~ the ~~respective~~ delay elements, respectively of the delay unit, and outputting phase information of the selected clock;

an interpolation factor output unit for converting the phase information into an interpolation factor, and outputting the interpolation factor; and

an interpolator for interpolating the video data signal read from the memory in accordance with the first clock signal using the interpolation factor.

4. (Currently Amended) A video signal processor comprising:

a memory that utilizes a first clock signal for writing ~~or reading~~ a video data signal to the memory or reading a video data signal from the memory;

a delay unit, including plural delay elements ~~having that can vary~~ respective variable delay values, for delaying the first clock signal by one period of the first clock signal;

a phase comparator for comparing phases between a clock that is obtained by delaying a focus clock in the first clock signal by one clock using the delay unit, and a clock that is one clock later in the first clock signal than the focus clock;

a controller for controlling the respective variable delay values of the delay elements of the delay unit on the basis of a phase difference detected by the phase comparator;

a selector for selecting a clock that is most synchronized with a reference signal inputted from outside, from among clocks that have been delayed by the delay elements, respectively of the delay unit, and outputting phase information of the selected clock;

an interpolation factor output unit for converting the phase information into an interpolation

factor, and outputting the interpolation factor; and

an interpolator for ~~interpolation~~interpolating the video data signal read from the memory in accordance with the first clock signal using the interpolation factor.

5. (Currently Amended) A video signal processing method ~~comprising~~including the steps of:
  - writing a video data signal into a memory in accordance with a first clock signal;
  - delaying a second clock signal using plural delay elements;
  - selecting a clock that is most synchronized with a reference signal inputted from outside, from among clocks that have been delayed by the delay elements, respectively, and generating phase information of the selected clock;
  - converting the phase information into an interpolation factor; and
  - interpolating the video data signal read from the memory in accordance with the second clock signal using the interpolation factor.